FIG. 1

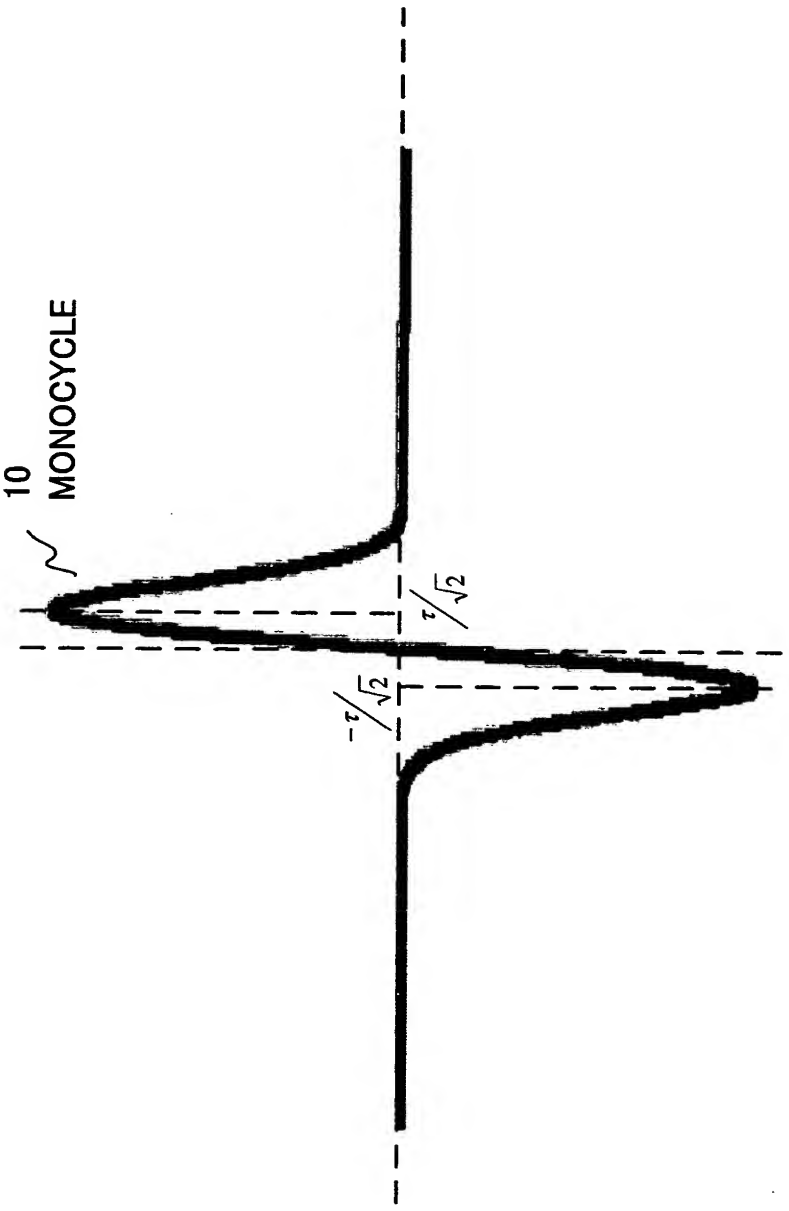

FIG. 2

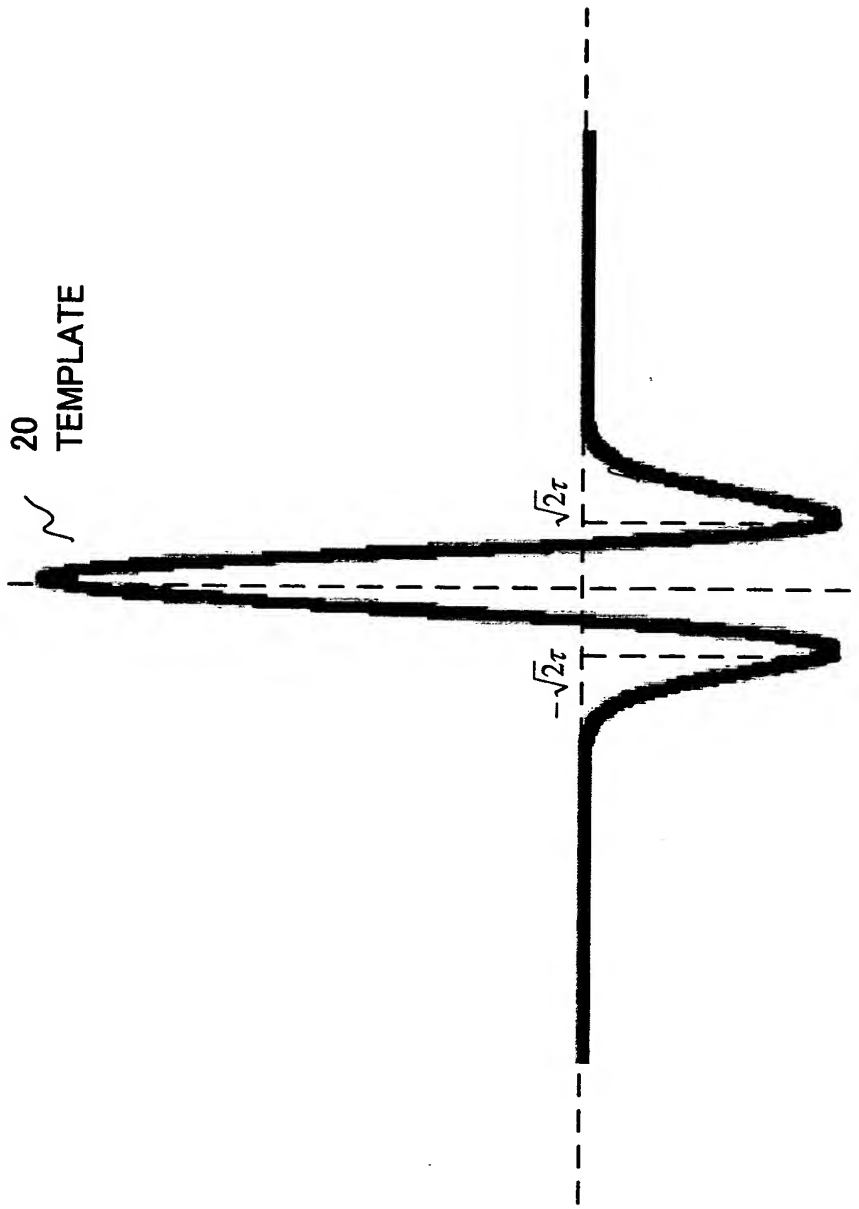

FIG. 3

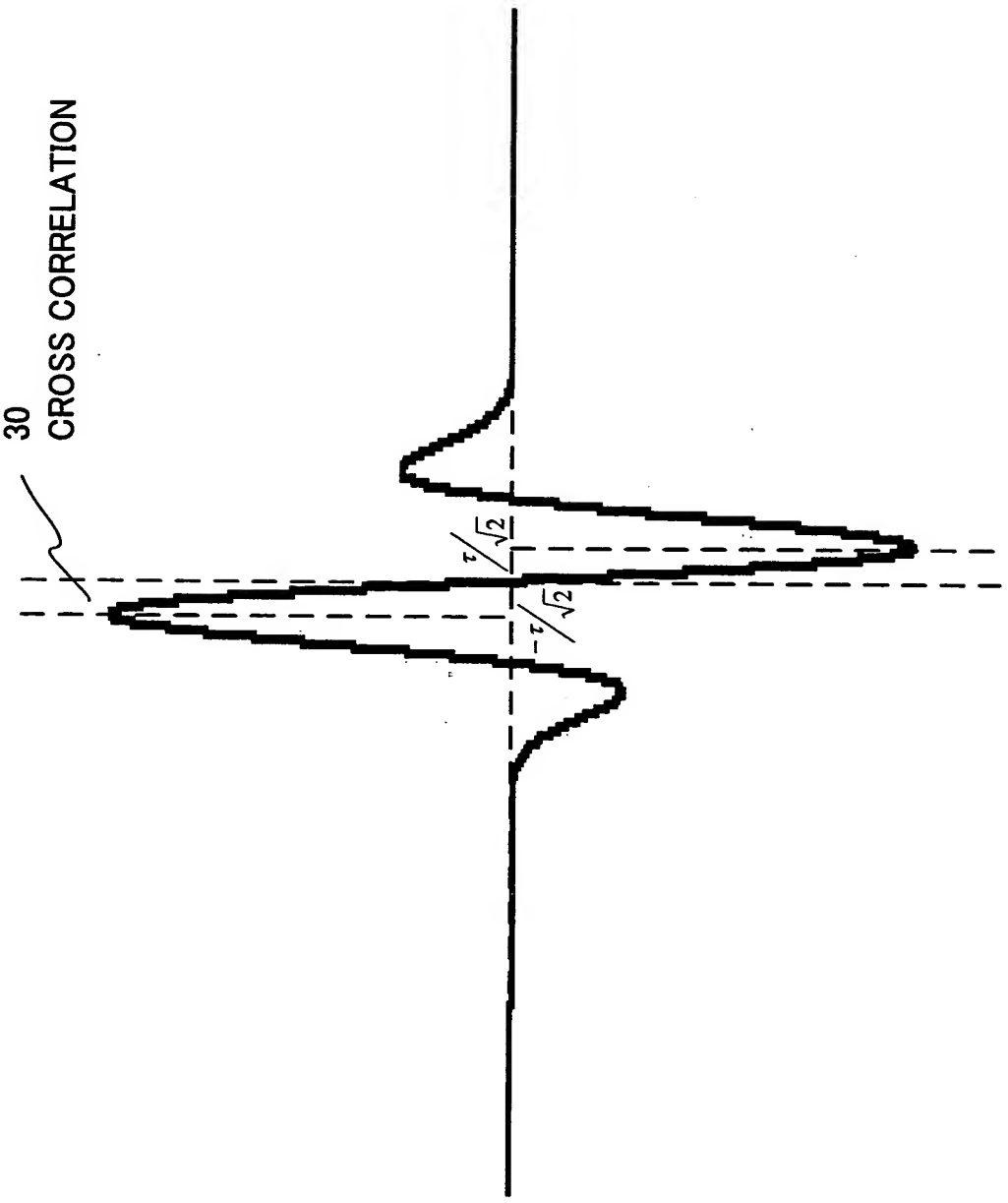

FIG. 4

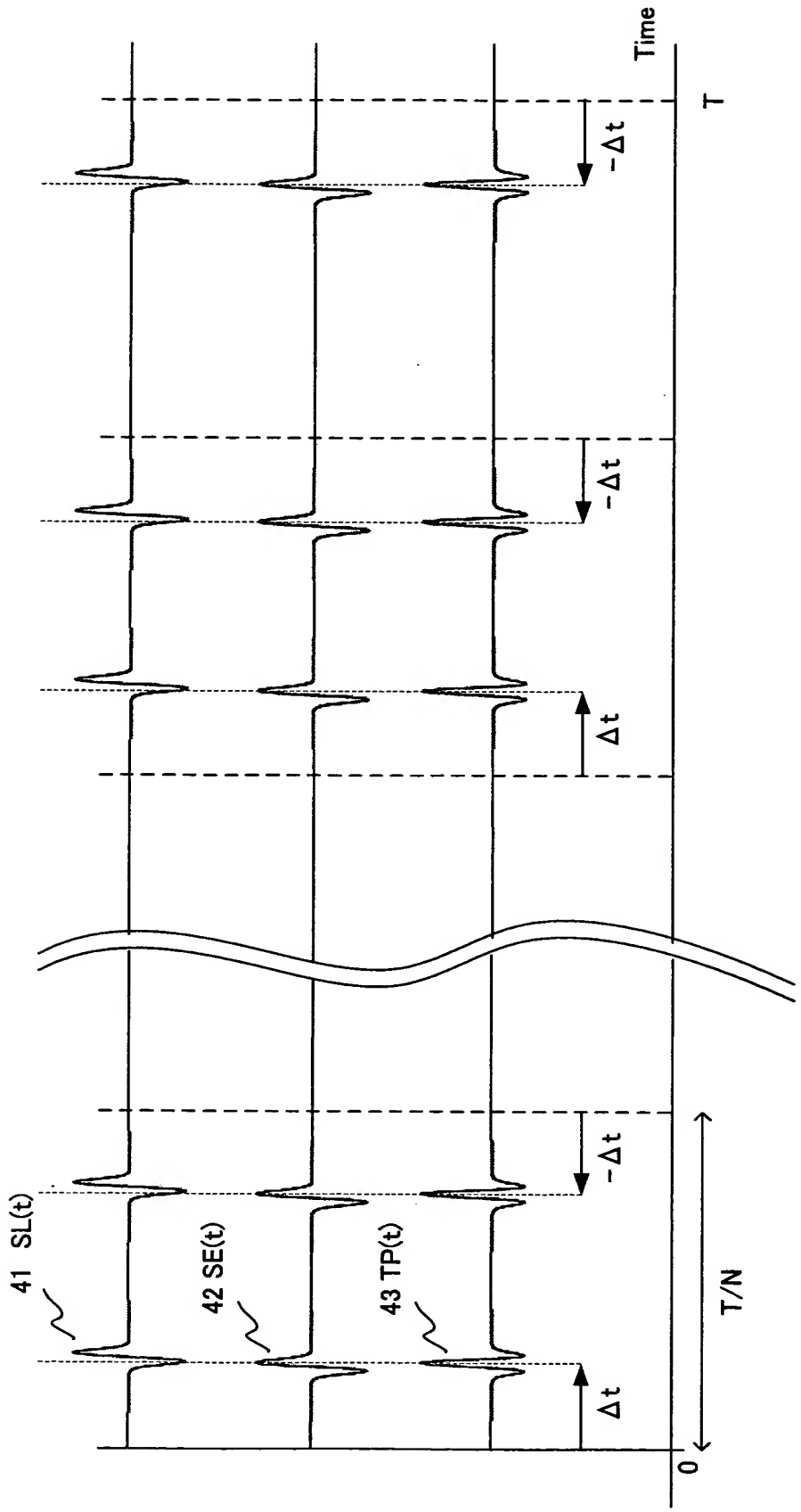

FIG. 5

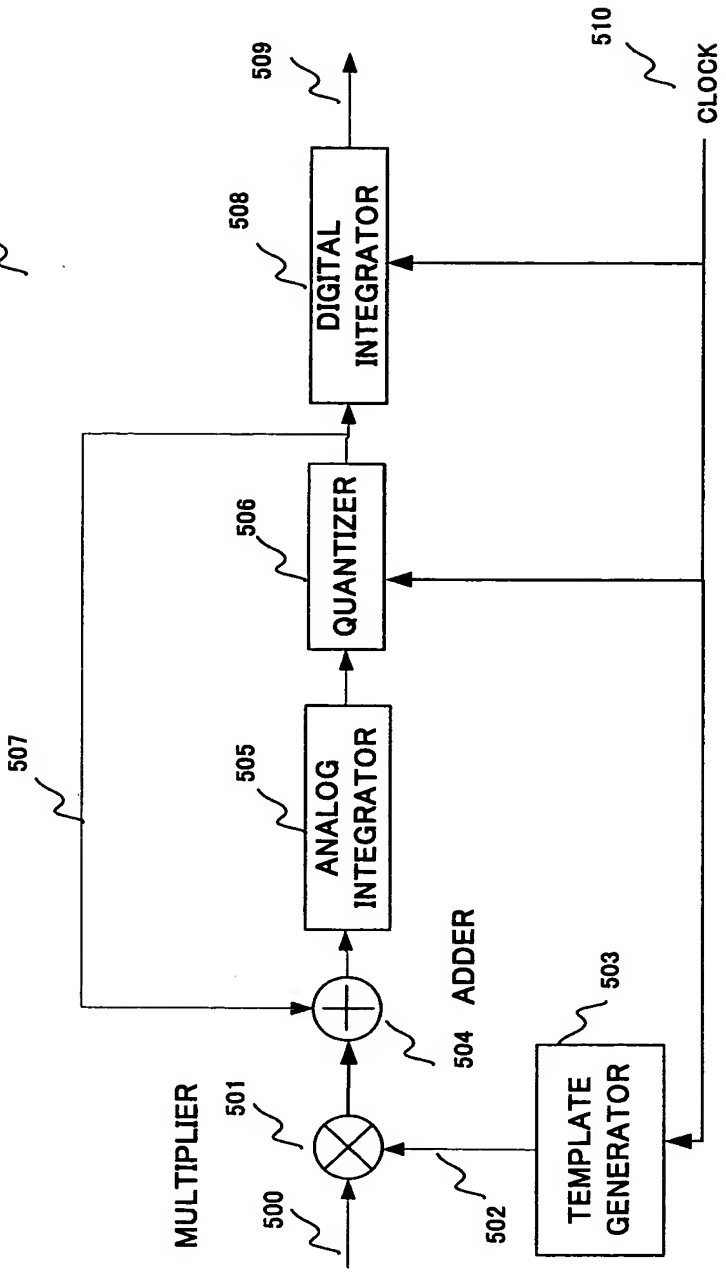
50
500
MULTIPLIER
501
502
TEMPLATE GENERATOR
503
ADDER
504
505
ANALOG INTEGRATOR
506
QUANTIZER
507
508
DIGITAL INTEGRATOR
509
510
CLOCK

FIG. 6

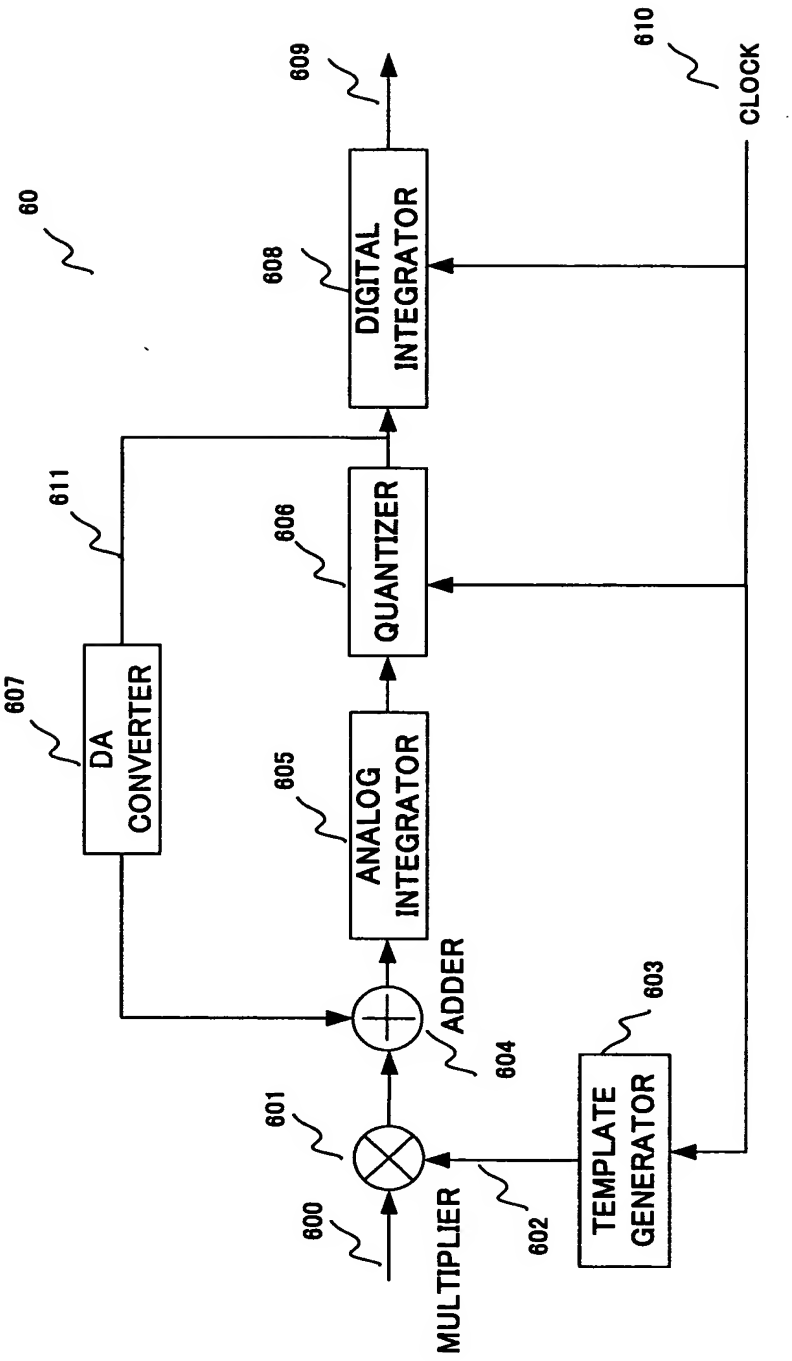
60
600
MULTIPLIER
601
602
TEMPLATE GENERATOR
603
ADDER
604
ANALOG INTEGRATOR
605
QUANTIZER
606
DA CONVERTER
607
DIGITAL INTEGRATOR
608
609
CLOCK
610
611

FIG. 7

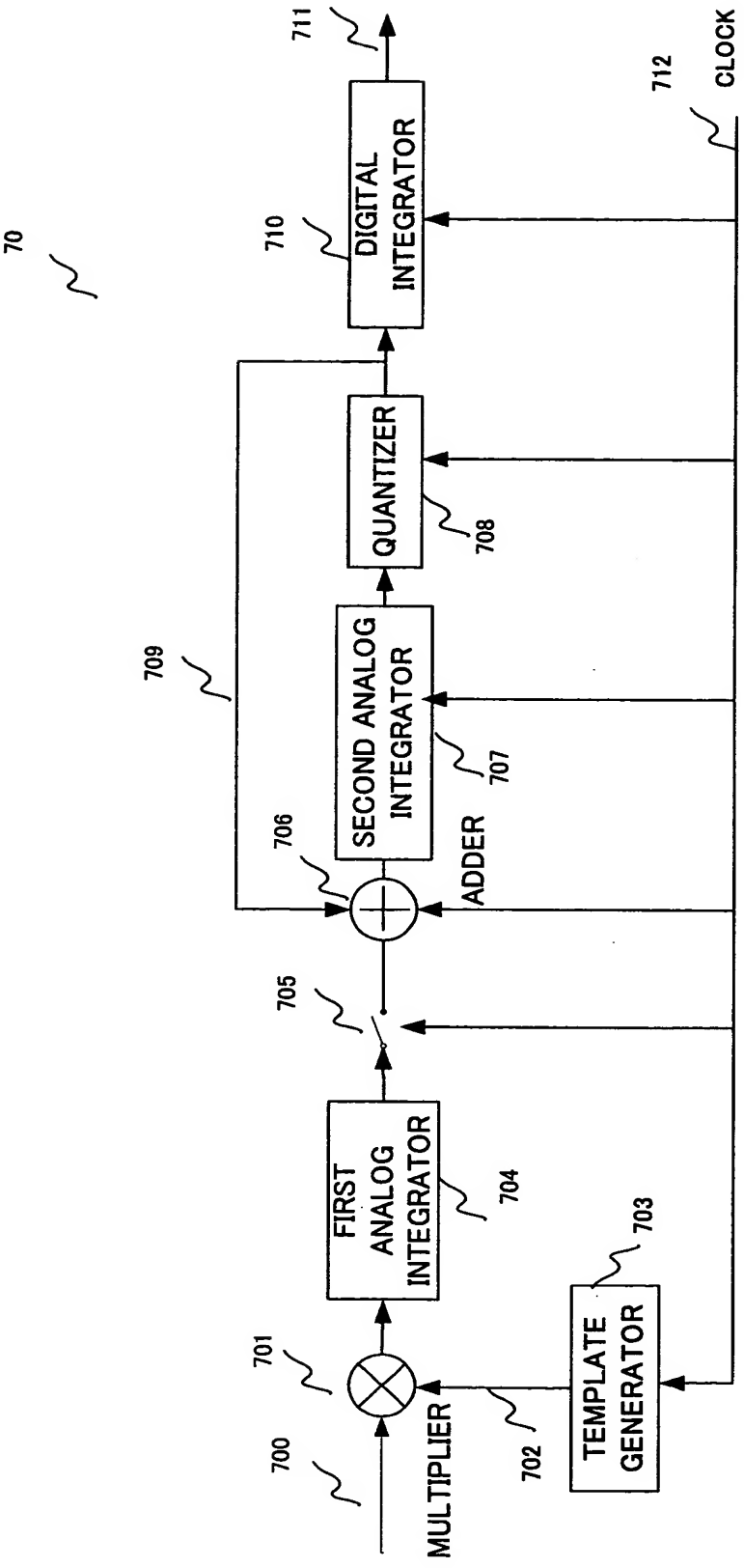
70
700
701
MULTIPLIER
702
TEMPLATE GENERATOR
703
FIRST ANALOG INTEGRATOR
704
705
706
ADDER
SECOND ANALOG INTEGRATOR
707
708
QUANTIZER
709
710
DIGITAL INTEGRATOR
711
712
CLOCK

FIG. 8

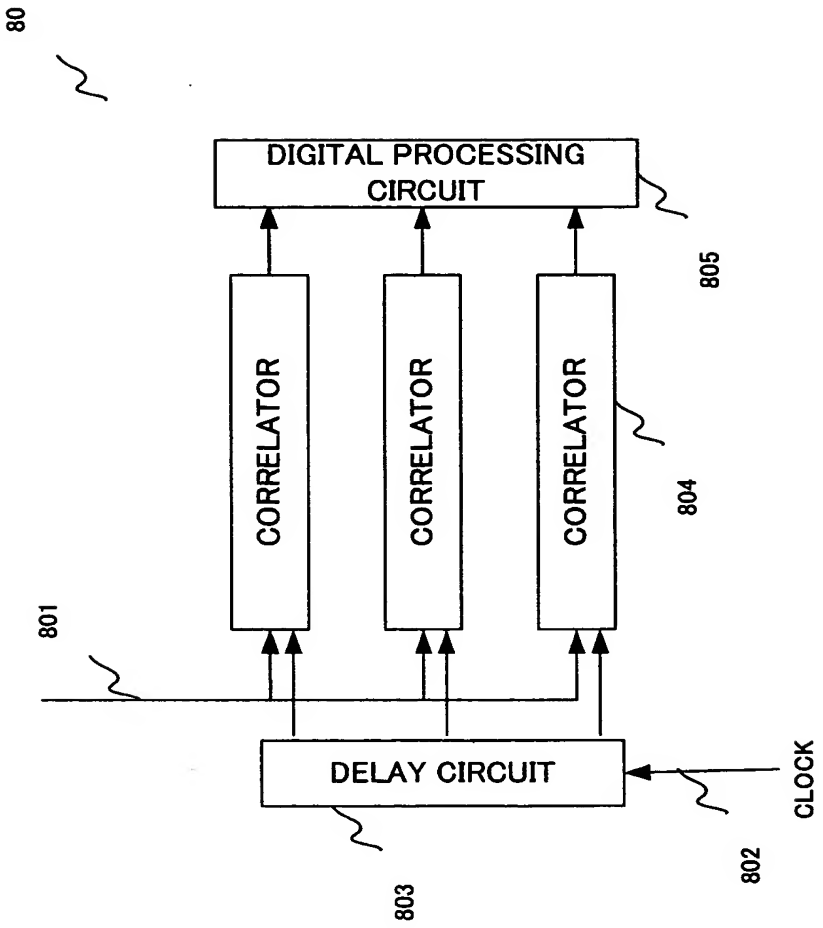
80
DIGITAL PROCESSING CIRCUIT
805
CORRELATOR
CORRELATOR
CORRELATOR
804
801
DELAY CIRCUIT
CLOCK
802
803

FIG. 9

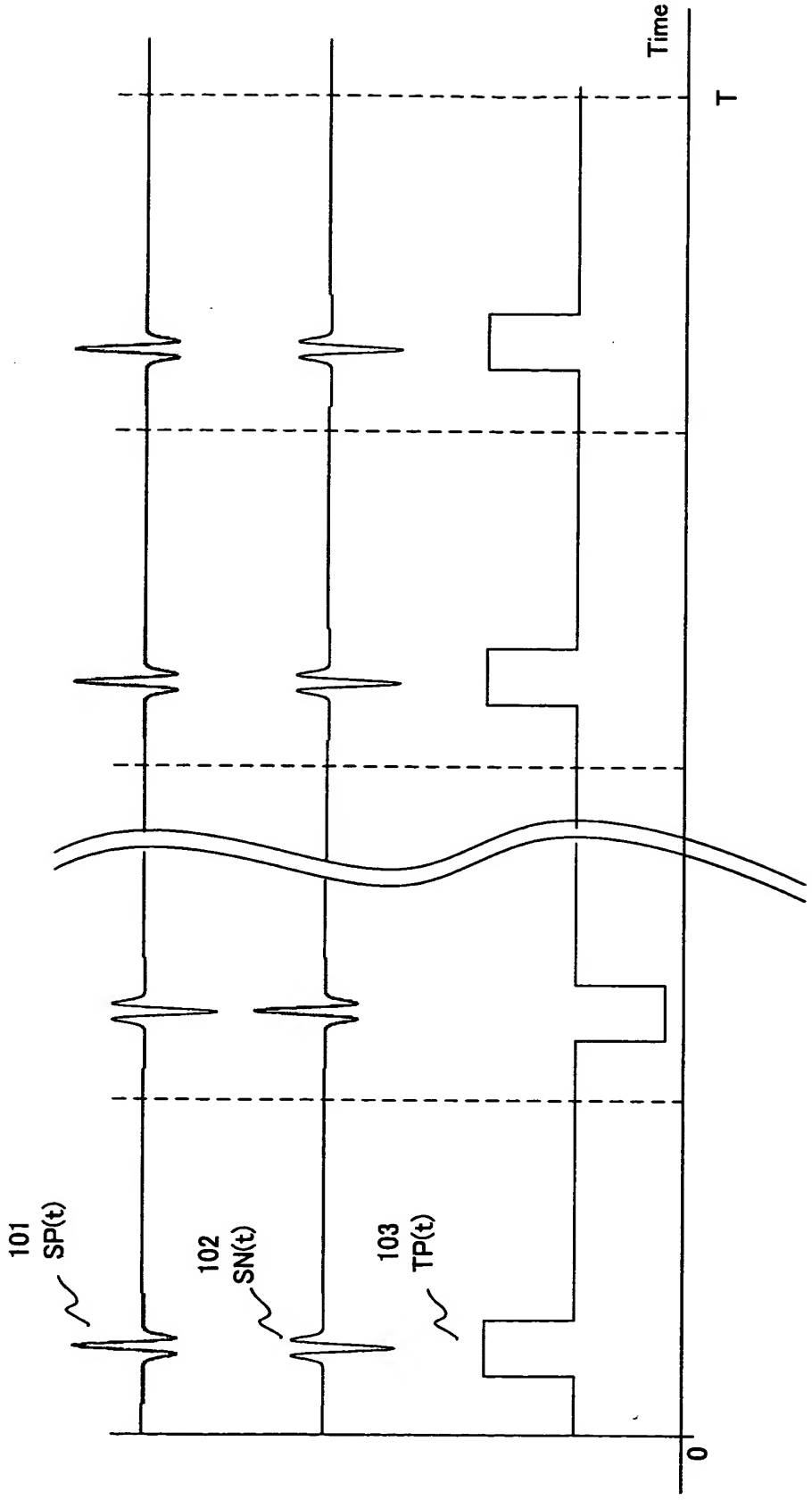
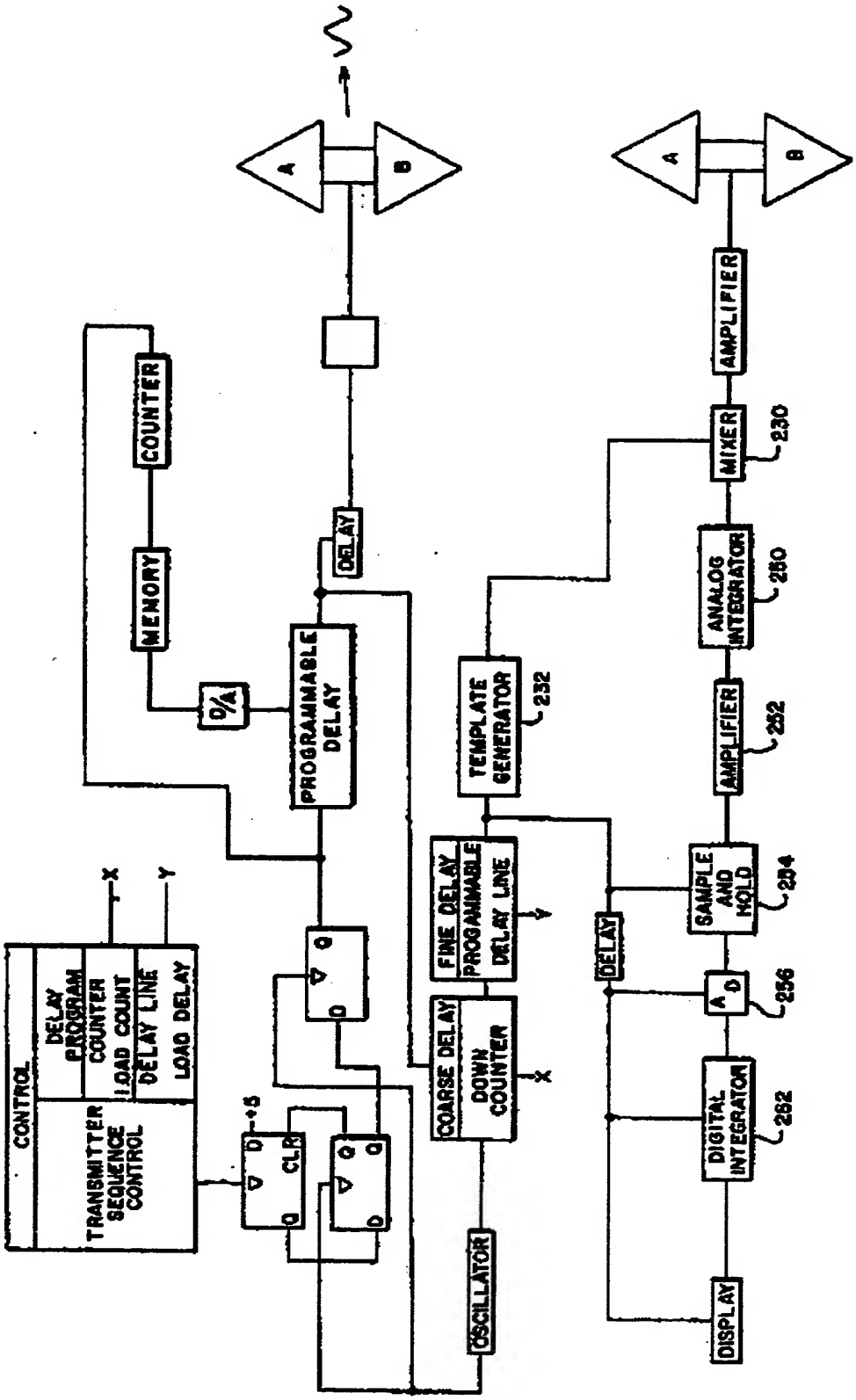
FIG. 10 PRIOR ART